



ASA-936-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Y. KAMIGAKI et al

Serial No. 10/663,779

Group Art Unit: 2926

Filed: September 17, 2003

Examiner: K. Quinto

For: SEMICONDUCTOR INTEGRATED CIRCUIT

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed March 24, 2004,  
Applicants respond as follows. A Petition and fee for a  
three-month Extension of Time also accompanies this response.

**IN THE TITLE**

Please amend the title as follows: SEMICONDUCTOR  
INTEGRATED CIRCUIT HAVING TWO SWITCH TRANSISTORS FORMED  
BETWEEN TWO DIFFUSION-LAYER LINES.